

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10599215	
	Filing Date		2006-09-22	
	First Named Inventor	RADULESCU		
	Art Unit			
	Examiner Name			
Attorney Docket Number		NL040302		

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1	XU, JUN: "ASYNCHRONOUS INTERCONNECTION AND INTERFACING OF INTELLECTUAL PROPERTY CORES IN THE DESIGN OF SYSTEMS-ON-CHIP"; CHAPTERS 5 & 6; "ONLINE"; 1 JULY 2002; CENTRE FOR CONCURRENT SYSTEMS AND VLSI, SCHOOL OF COMPUTING INFORMATION SYSTEMS AND MATHEMATICS, SOUTH BANK UNIVERSITY; XP002333385.	<input type="checkbox"/>
2	RADULESCU A. ET AL: "AN EFFICIENT ON-CHIP NETWORK INTERFACE OFFERING GUARANTEED SERVICES, SHARED-MEMORY ABSTRACTION, AND FLEXIBLE NETWORK CONFIGURATION"; PROCEEDINGS OF THE DESIGN, AUTOMATION AND TEST IN EUROPE CONFERENCE AND EXHIBITION, 2004; PROCEEDINGS FEB. 16-20, 2004, PISCATAWAY, NJ, USA, IEEE 16 FEBRUARY 2004, PP.878-883; XP010684780.	<input type="checkbox"/>

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

- ☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☒ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Edward W. Goodman/	Date (YYYY-MM-DD)	2007-04-18
Name/Print	EDWARD W. GOODMAN	Registration Number	28613

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